



First Named Inventor	Christophe Chevallier
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Examiner Name	Unknown - V. Nguyen
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**SUPPLEMENTAL
INFORMATION DISCLOSURE
STATEMENT
FORM PTO-1449**

Title: FLASH ARRAY IMPLEMENTATION WITH LOCAL AND GLOBAL BIT LINES

U.S. Patent References				
Examiner Initials	Document No.	Issue/Published Date	Name	Filing Date
VP	5,777,922	July 7, 1998	Choi	October 18, 1996
VP	5,894,437	April 13, 1999	Chang	January 23, 1998

Foreign Patent References				
Examiner Initials	Foreign Patent		Name	Publication Date
	Country	No.		Translation
VP	WO	96/31882	Micron Technology, Inc.	10/10/96

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
	NONE

Viet Q. Nguyen
Primary Examiner

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Viet Q. Nguyen
Primary Examiner

Examiner Signature	V. Nguyen	Date Considered	5/20/04
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*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Based on Form PTO-FB-A820 Patent and Trademark Office, U.S. Department of Commerce